

Figure 1(a)
(Prior Art)

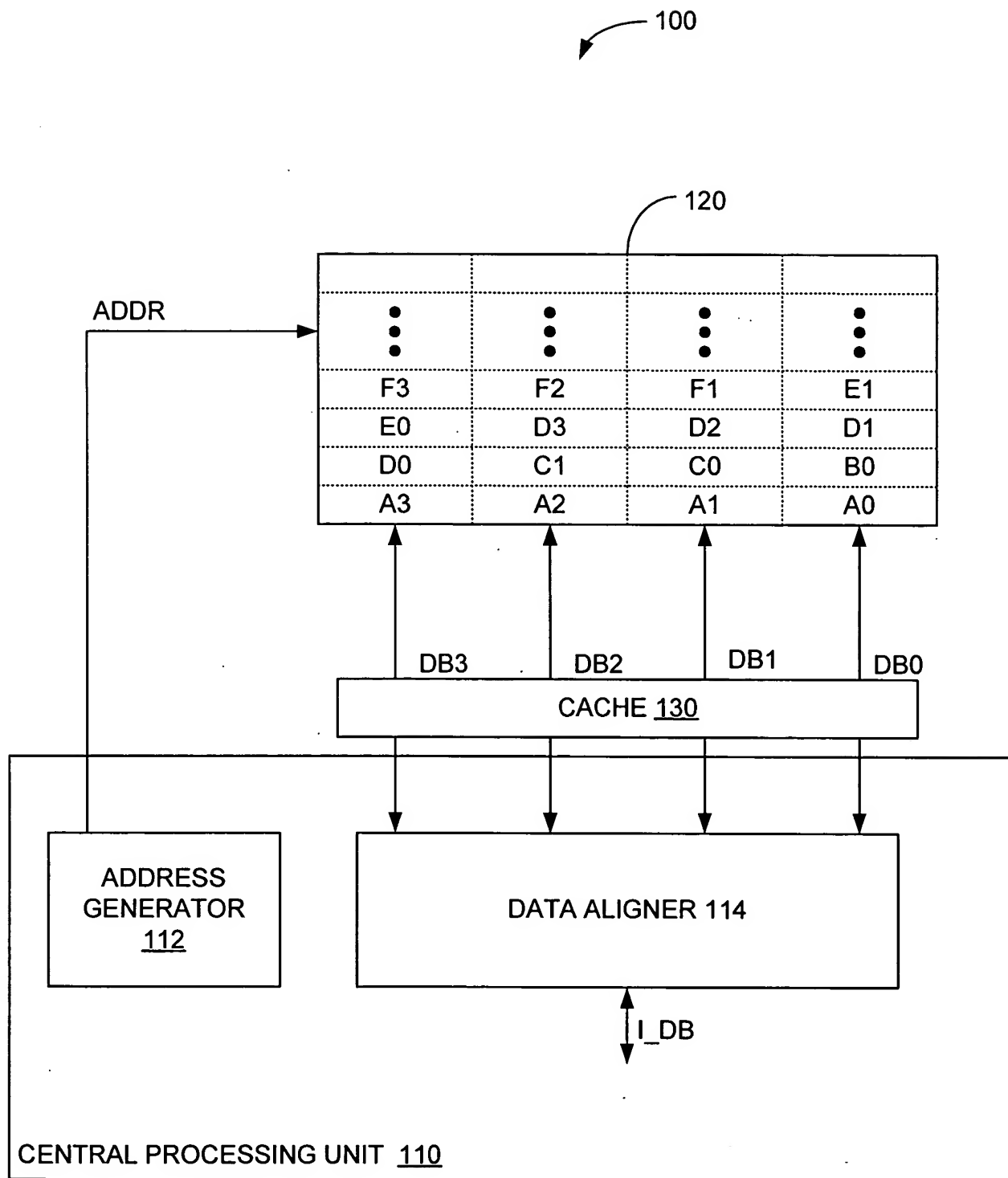


Figure 1(b)
(Prior Art)

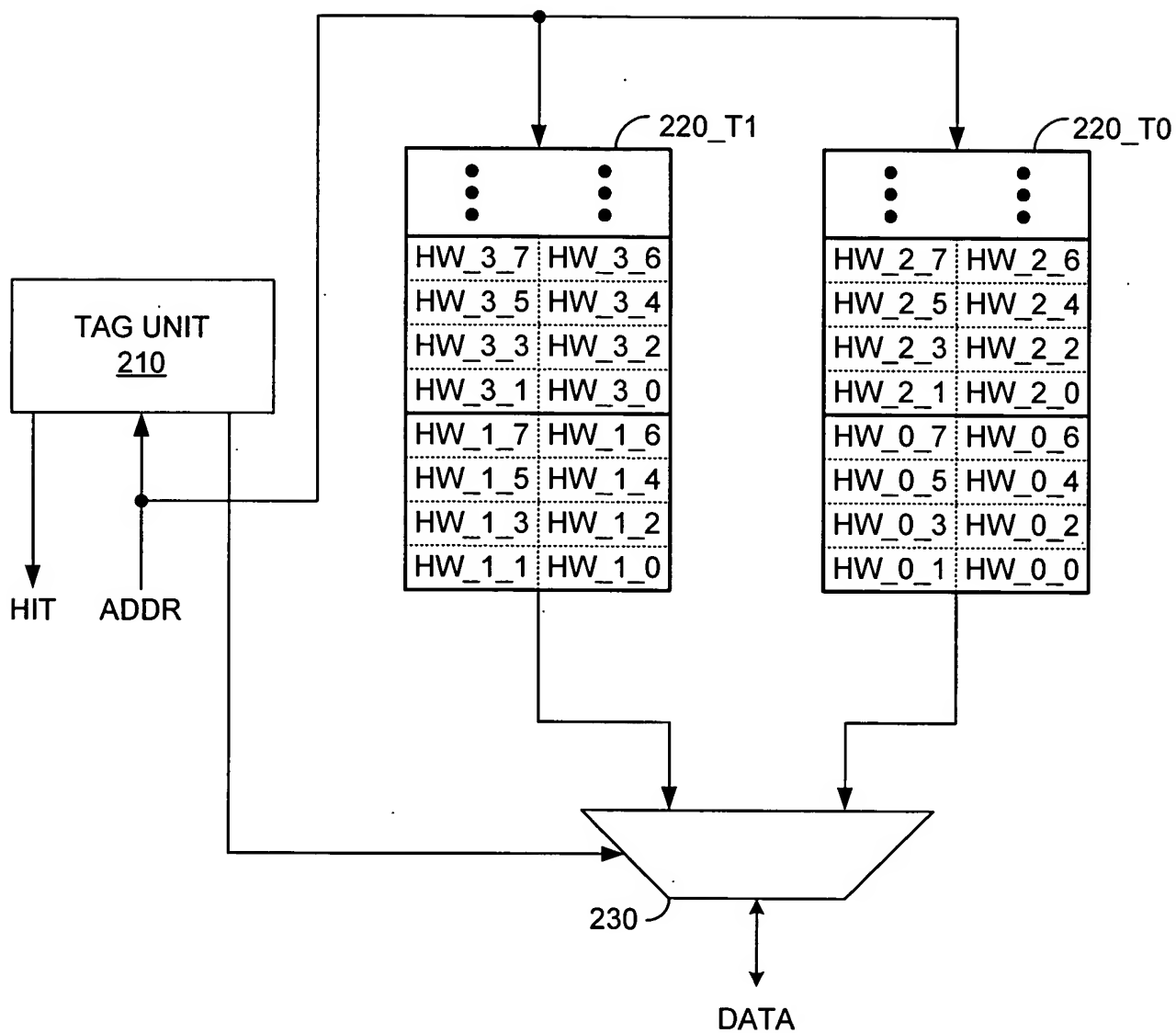


Figure 2

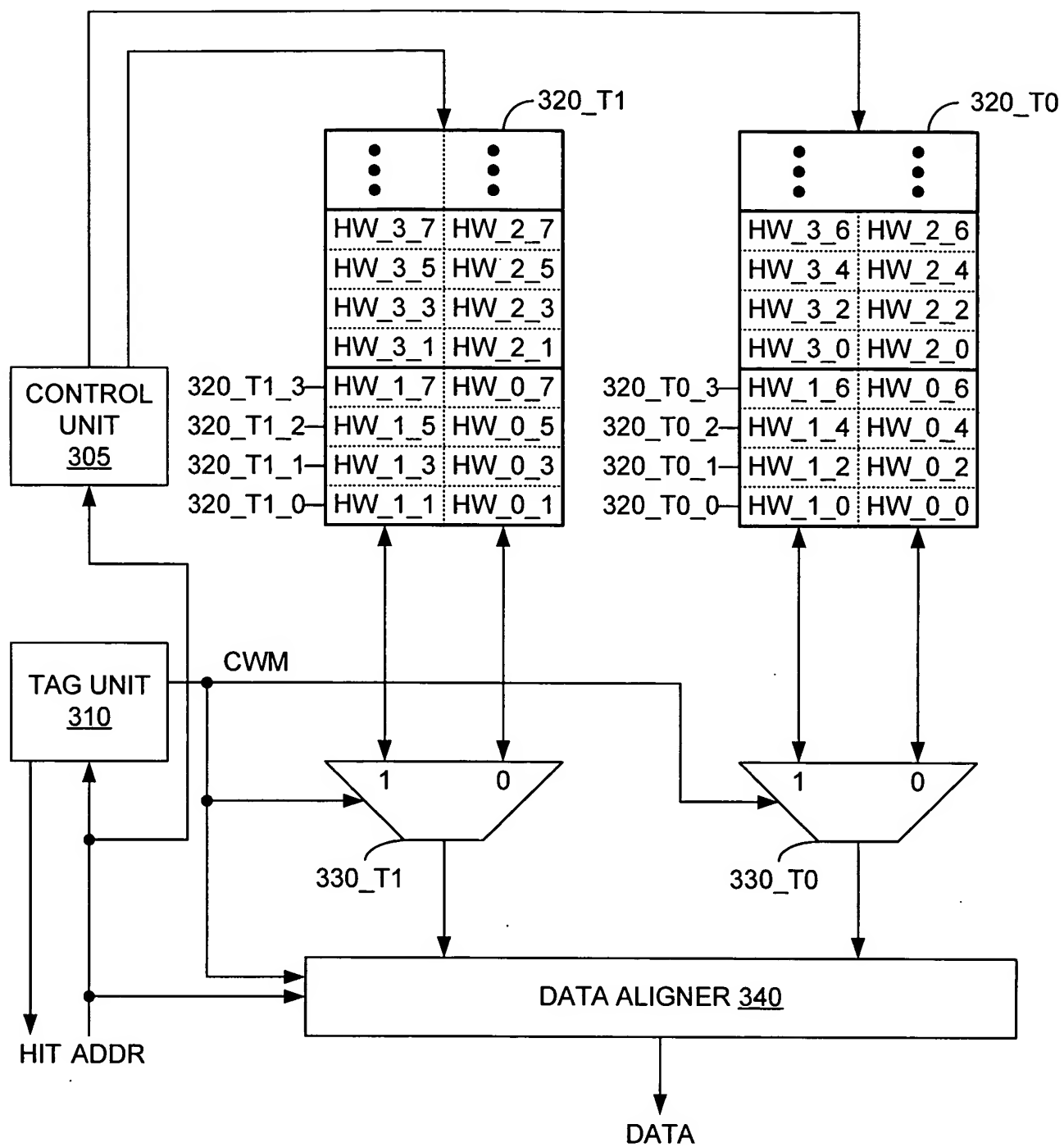


Figure 3

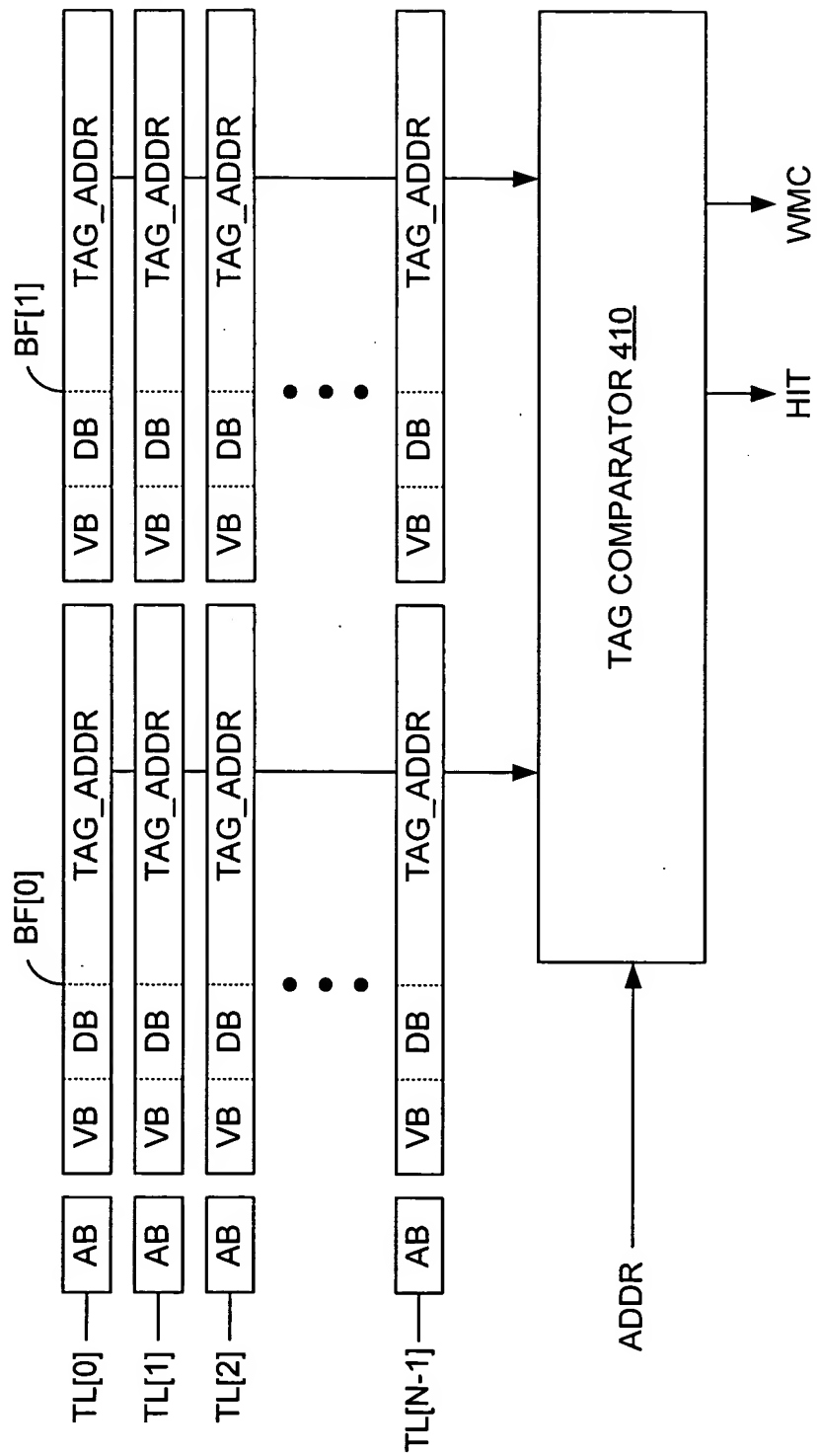


Figure 4

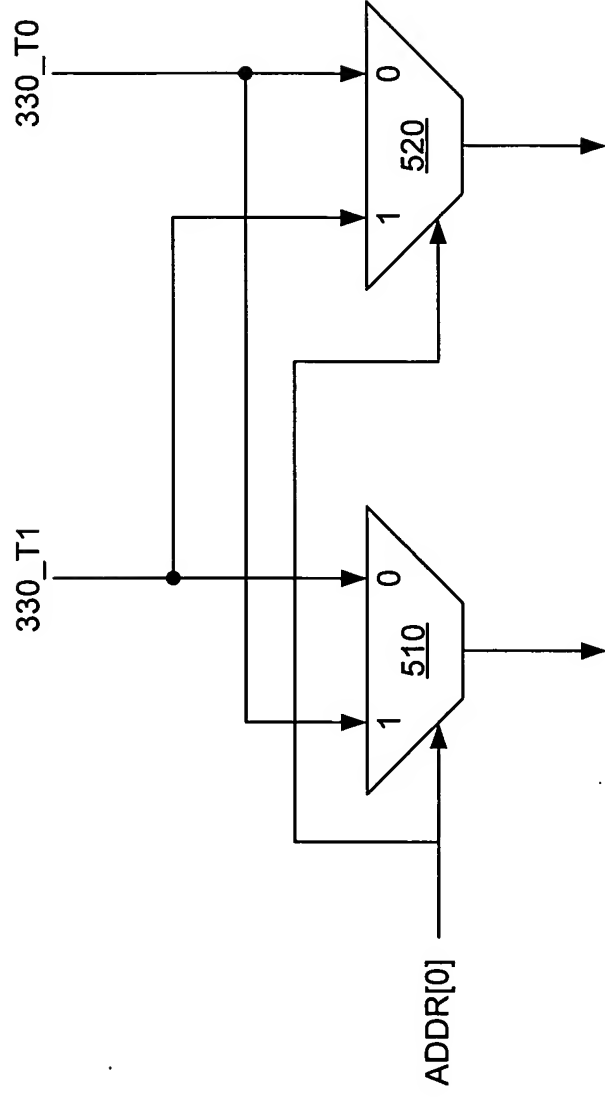


Figure 5

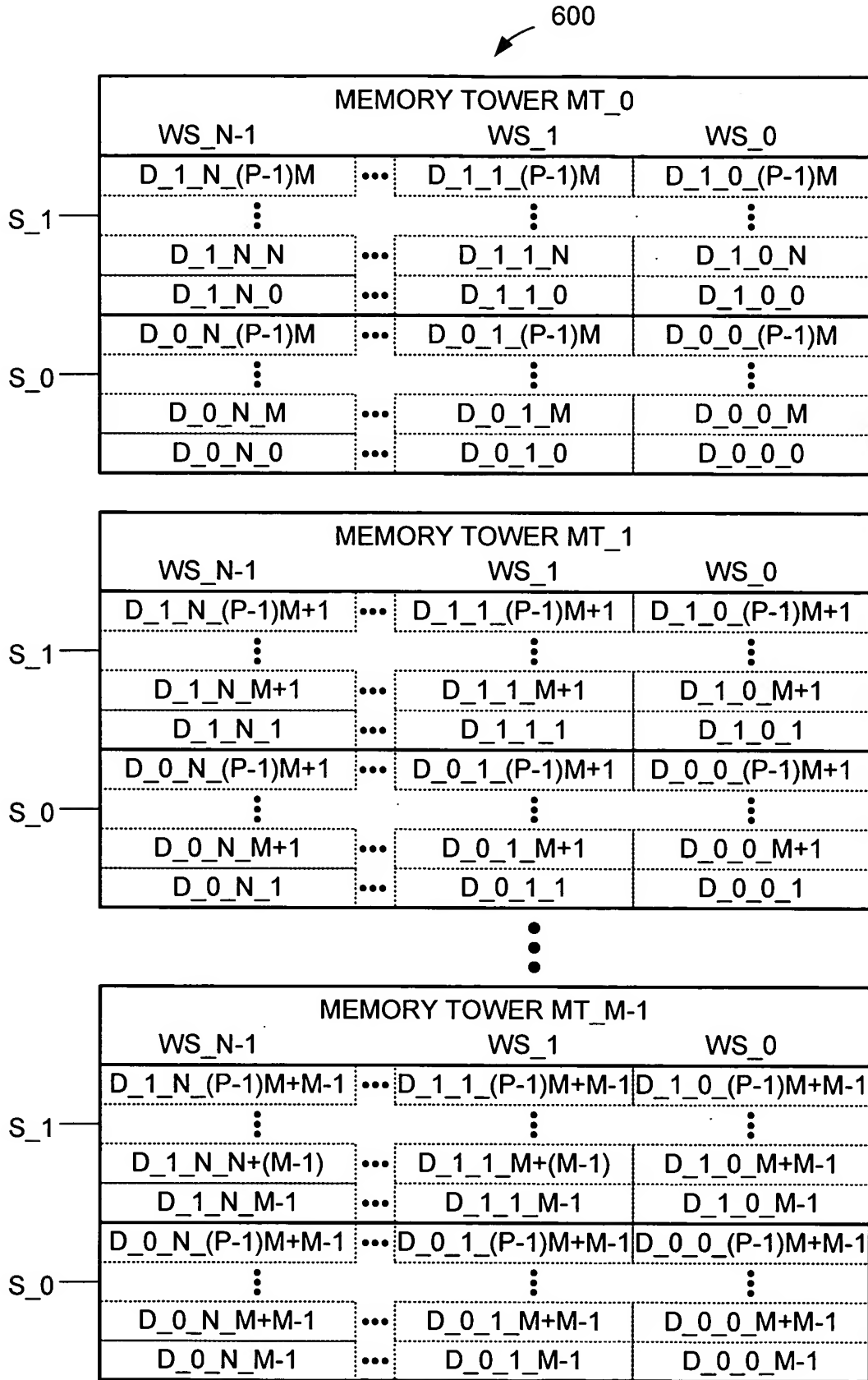


Figure 6(a)

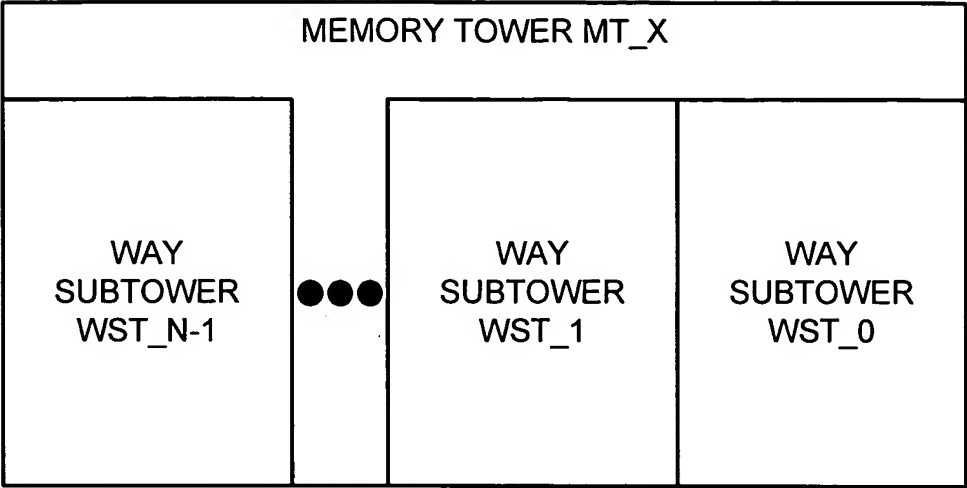


Figure 6(b)

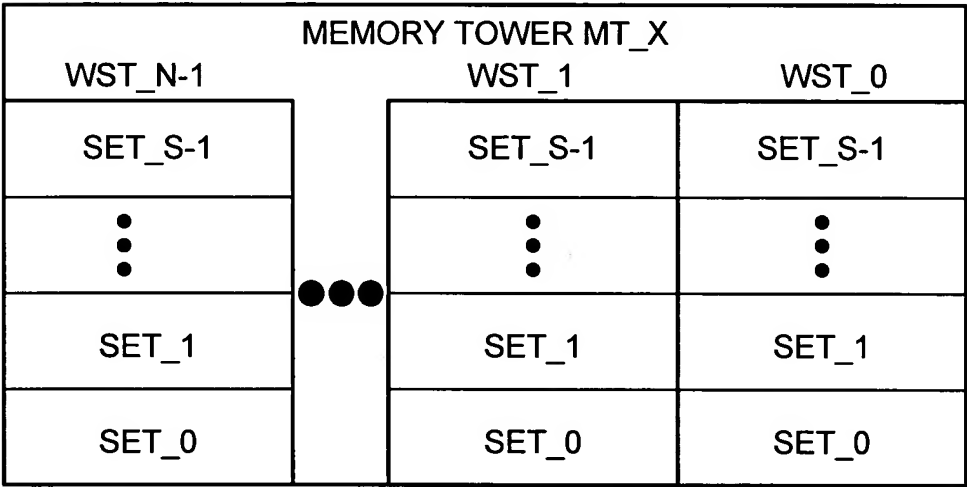


Figure 6(c)

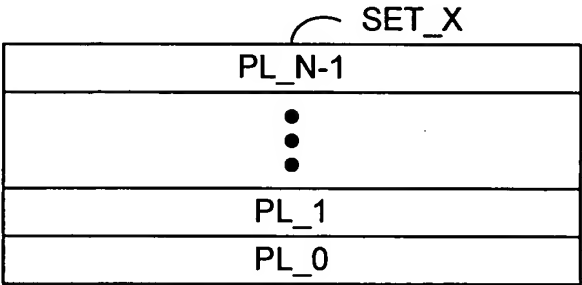



Figure 6(d)

600



MEMORY TOWER MT_1	
D_1_1_13	D_1_0_13
D_1_1_9	D_1_0_9
D_1_1_5	D_1_0_5
D_1_1_1	D_1_0_1
D_0_1_13	D_0_0_13
D_0_1_9	D_0_0_9
D_0_1_5	D_0_0_5
D_0_1_1	D_0_0_1

MEMORY TOWER MT_0	
D_1_1_12	D_1_0_12
D_1_1_8	D_1_0_8
D_1_1_4	D_1_0_4
D_1_1_0	D_1_0_0
D_0_1_12	D_0_0_12
D_0_1_8	D_0_0_8
D_0_1_4	D_0_0_4
D_0_1_0	D_0_0_0

MEMORY TOWER MT_3	
D_1_1_15	D_1_0_15
D_1_1_11	D_1_0_11
D_1_1_7	D_1_0_7
D_1_1_3	D_1_0_3
D_0_1_15	D_0_0_15
D_0_1_11	D_0_0_11
D_0_1_7	D_0_0_7
D_0_1_3	D_0_0_3

MEMORY TOWER MT_2	
D_1_1_14	D_1_0_14
D_1_1_10	D_1_0_10
D_1_1_6	D_1_0_6
D_1_1_2	D_1_0_2
D_0_1_14	D_0_0_14
D_0_1_10	D_0_0_10
D_0_1_6	D_0_0_6
D_0_1_2	D_0_0_2

Figure 7